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| SERIAL NUMBER 10/686,118 | FILING OR 371(c) DATE 10/14/2003 RULE | CLASS 711 | GROUP ART UNIT 2188 | ATTORNEY DOCKET NO. 500797.02 (30005/US/2) |
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APPLICANTS
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**** CONTINUING DATA *******
 This application is a CON of 09/896,030 06/28/2001 PAT 6,801,989 *GJP*

**** FOREIGN APPLICATIONS *******
 NONE *GJP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **
 01/28/2004

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| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Examiner's Signature</i> Initials | STATE OR COUNTRY ID | SHEETS DRAWING 7 | TOTAL CLAIMS 50 | INDEPENDENT CLAIMS 8 |
|--|---|-------------------------------|----------------------------|---------------------------|--------------------------------|

ADDRESS
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TITLE
 Method and system for adjusting the timing offset between a clock signal and respective digital signals transmitted along with that clock signal, and memory device and computer system using same

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